

PATENT APPLICATION  
Docket No. 9898-305  
Client No. SS-18660-US

Applicant: Seok-Jun Won Serial No. not yet assigned

Filing Date: September 30, 2003 Group: not yet assigned

Title: FLAT-TYPE CAPACITOR FOR INTEGRATED CIRCUIT AND METHOD OF  
MANUFACTURING THE SAME

INFORMATION DISCLOSURE CITATION  
FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub</u> <u>Class</u>
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FOREIGN PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
<u>Loke</u>	_____	JP2001-308287	1/11/2001	Japan	

OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
<u>Loke</u>	_____	English Language of Abstract for Japanese Patent Publication No. JP2001-308287, filed 1/11/2001.
<u>Loke</u>	_____	English Language of Abstract for Japanese Patent Publication No. JP9162369, 6/20/1997.

Examiner: Loke

Date Considered: 11/28/04